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Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: )  
Pippin, Jack ) Examiner: Phan, Thai  
Application No: 08/660,016 ) Art Unit: 2763  
Filed: June 6, 1996 )  
Allowance Date: June 11, 1998 )  
Batch No: F24 )  
For: METHOD AND APPARATUS )  
FOR PROGRAMMABLE )  
THERMAL SENSOR FOR AN )  
INTEGRATED CIRCUIT )  
Continuation of: )  
Application No: 08/124,980 )  
Filed: September 21, 1993 )  
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SUBMISSION OF FORMAL DRAWINGS

Sir:

In response to the Notice of Allowance mailed June 11, 1998, applicants  
have enclosed formal drawings of Figures 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, and 11  
(11 sheets) for the above-identified U.S. patent application.

If there are any additional charges associated with this communication,  
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Respectfully submitted,  
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Date July 28, 1998 William A. Davis  
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